



**SRINIVASA INSTITUTE OF TECHNOLOGY & SCIENCE**  
(AUTONOMOUS)

Chennai-Hyderabad Bypass Road, Ukkayapalli, Kadapa, A.P-516002



**Examinations Branch**

**M.Tech I Semester (SITS24) Supplementary Examinations August-2026**

**Time: 10:00AM to 01:00PM**

| Date/Day                 | ELECTRICAL POWER SYSTEMS                          | VLSI SYSTEM DESIGN                             | COMPUTER SCIENCE & ENGINEERING                      |
|--------------------------|---------------------------------------------------|------------------------------------------------|-----------------------------------------------------|
| 18.08.2026<br>(Tuesday)  | Advanced PowerSystemProtection<br>24MEP101        | COMS Analog IC Design<br>24MVD101              | Advanced Data Structures and Algorithms<br>24MCS101 |
| 20.08.2026<br>(Thursday) | PowerSystemSecurityandStateEstimation<br>24MEP102 | COMS Digital IC Design<br>24MVD102             | Advanced Computer Networks<br>24MCS102              |
| 25.08.2026<br>(Tuesday)  | Energy Auditing and Management<br>24MEP103a       | Microchip fabrication Techniques<br>24MVD103a  | Object Oriented Software Engineering<br>24MCS103b   |
| 27.08.2026<br>(Thursday) | SmartGridTechnologies<br>24MEP104b                | FPGA Architecture and Application<br>24MVD104b | Design Patterns<br>24MCS104b                        |
| 29.08.2026<br>(Saturday) | ResearchMethodologyandIPR<br>24MRM101             |                                                |                                                     |

Notification Date: 21.07.2026

Sd/-  
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Examination